



N 10/651,849

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Paul M. Henry Examiner: Terry D. Cunningham  
Serial No.: 10/651,849 Group Art Unit: 2816  
Filed: August 29, 2003 Docket No.: 50019.242US01/P05640  
Title: NULLED ERROR AMPLIFIER

CERTIFICATE UNDER 37 CFR 1.10:

"Express Mail" mailing label number: EV 705470393 US

Date of Deposit: March 24, 2006

I hereby certify that this paper or fee is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

By:

Name: Alice Baum

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT (37 C.F.R. § 1.97(c))**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Dear Sir:

With regard to the above-identified application, the items of information listed on the enclosed Form 1449 are brought to the attention of the Examiner.

This statement should be considered because it is submitted after the mailing date of a final action under 37 C.F.R. § 1.113 or after the mailing date of the Notice of Allowance under 37 C.F.R. § 1.311 or after any other action that closes prosecution on the application, but before the payment of the issue fee. Please Charge Deposit Account 13-2725 in the amount of \$180.00 in payment of the fee as set forth in 37 C.F.R. § 1.17(p). This statement is submitted as certified below under 37 C.F.R. § 1.97(e)(1) or (2) by the undersigned.

**Certification Under 37 C.F.R. § 1.97(e)(2)**

In accordance with 37 C.F.R. § 1.97(c) or § 1.97(d), the undersigned hereby certifies that no item of information listed on the enclosed Form 1449 was cited in a communication from a foreign patent office in a counterpart foreign application, and to the knowledge of the

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undersigned after making a reasonable inquiry, no item of information listed on the enclosed Form 1449 was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this statement.

In accordance with 37 C.F.R. §1.98(a)(2), a copy of each document or other information listed on the enclosed Form 1449 is provided.

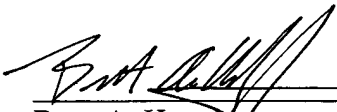
No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103 and Applicant reserves the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish that the reference(s) are not "prior art." Moreover, Applicant does not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended.

Consideration of the items listed is respectfully requested. Pursuant to the provisions of M.P.E.P. 609, it is requested that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

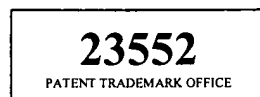
Please charge any additional fees or credit any overpayment to Deposit Account No. 13-2725.

Respectfully submitted,

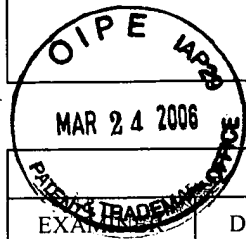
MERCHANT & GOULD

  
Brett A. Hertzberg  
Registration No. 42,660  
Direct Dial: 206.342.6255

MERCHANT & GOULD  
P.O. Box 2903  
Minneapolis, Minnesota 55402-0903  
206.342.6200  
[BAH/ab]



<b>FORM 1449*</b> <b>INFORMATION DISCLOSURE STATEMENT</b>  <b>IN AN APPLICATION</b> (Use several sheets if necessary)	Docket Number: 50019.242US01/P05640	Application Number: 10/651,849
	Applicant: Paul M. Henry	
	Filing Date: August 29, 2003	Group Art Unit: 2816



U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,847,670	12/08/98	Gratrex et al.	341	159	

FOREIGN PATENT DOCUMENTS							
	DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)		
		Grebene, Alan, "Bipolar and MOS Analog Integrated Circuit Design," <i>John Wiley &amp; Sons</i> , 1939, 1984 (9 pgs)
		Razavi, Behzad, "Design of Analog CMOS Integrated Circuits," <i>McGraw Hill</i> , 2001, (5 pgs)
		Li, Tao, et al. A Low Voltage Bandgap Reference Circuit with Current Feedback," <i>University of Michigan Design Contest Report</i> , Fall, 2003 (5 pgs)
		Gupta, Vishal, et al., "Predicting and Designing for the Impact of Process Variations and Mismatch on the Trip Range and Yield of Bandgap References," <i>IEEE Quality of Electronic Design Symposium</i> , March 21-23, 2005 (7 pgs)
		"Offset Null," <i>Answers Corporation</i> , 2006 (2 pgs)



EXAMINER	DATE CONSIDERED
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form for next communication to the Applicant.	